

Adapting TDL to Provide Testing Support for Executable DSLs

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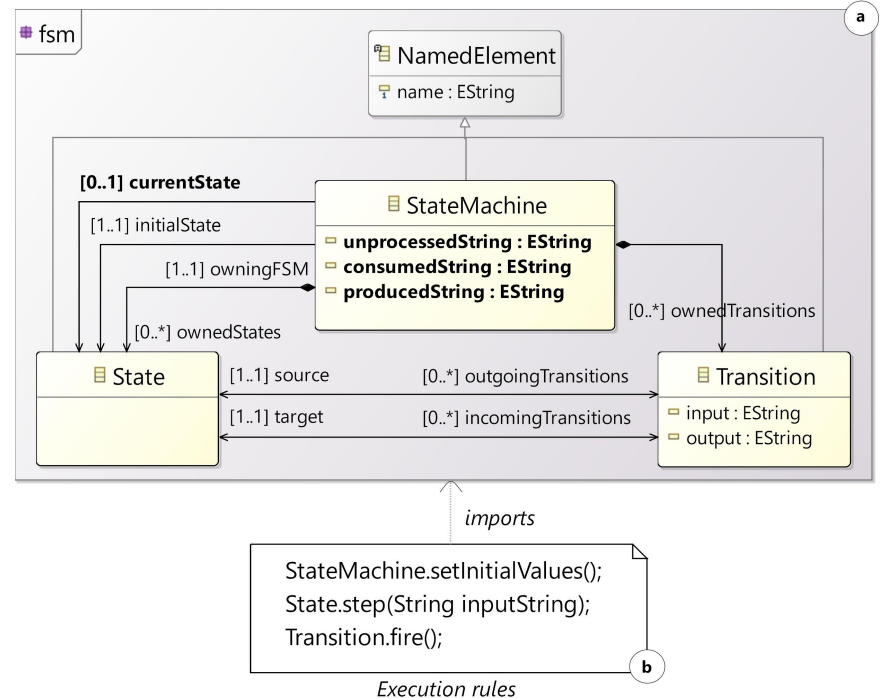
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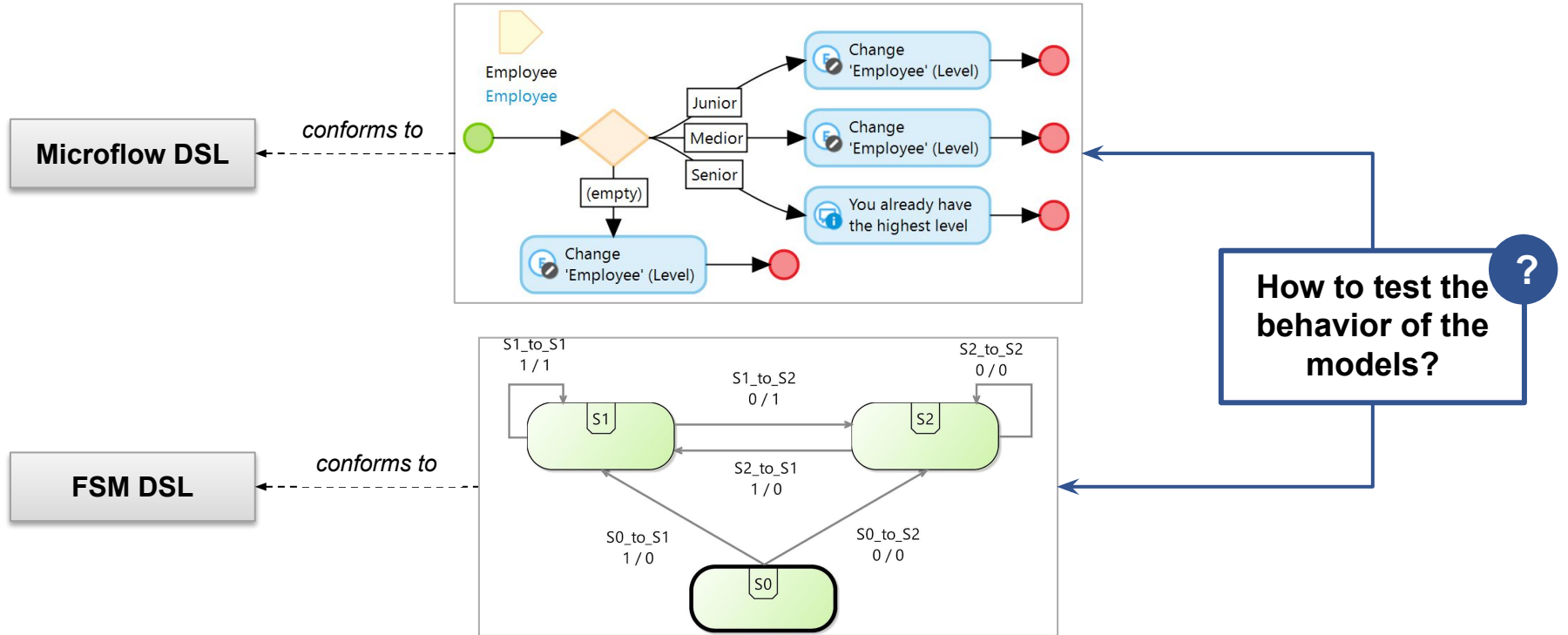
Context: Executable DSLs

Composed of two parts:

1. *Abstract Syntax*: Defining the domain concepts
2. *Operational Semantics* (Interpreter)
 - a. the definition of the possible **runtime states** of a model under execution
 - b. a set of **execution rules** that define how such a runtime state changes over time

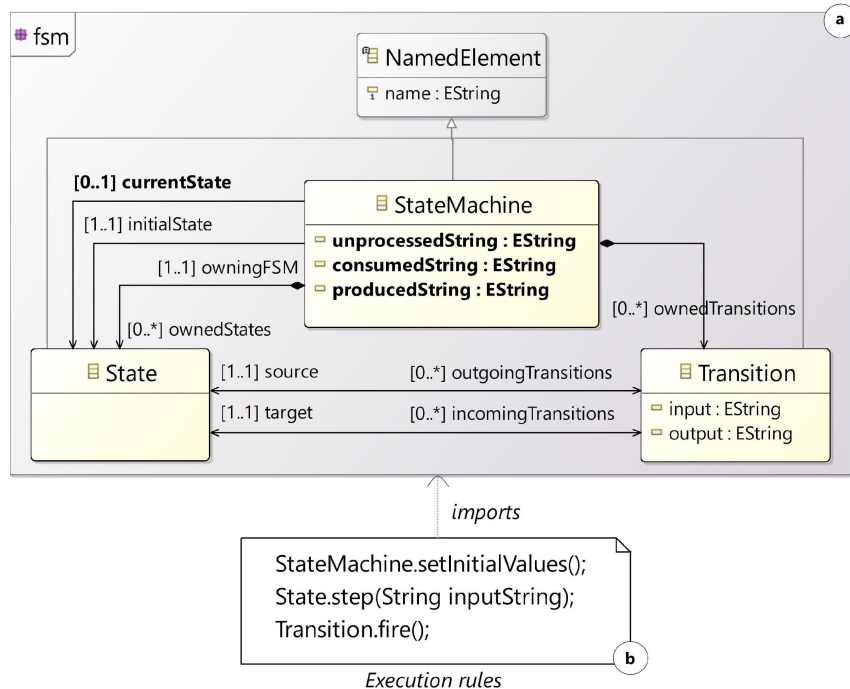


Heterogeneity of the DSLs

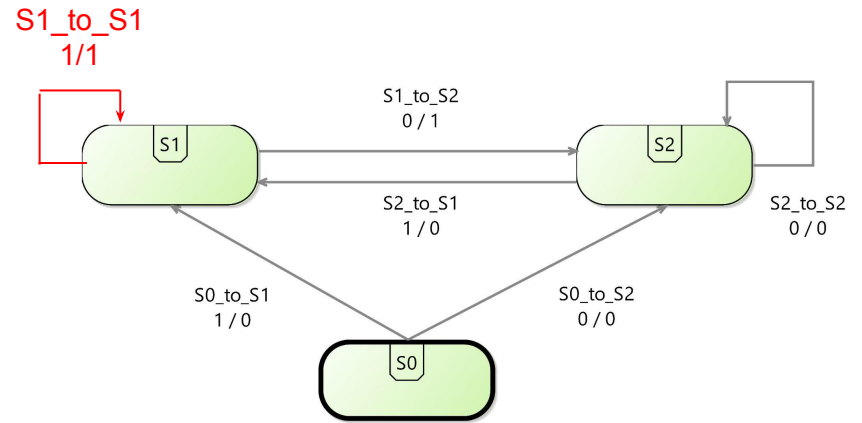


Running Example: DSL and Model

Executable FSM DSL (xFSM)

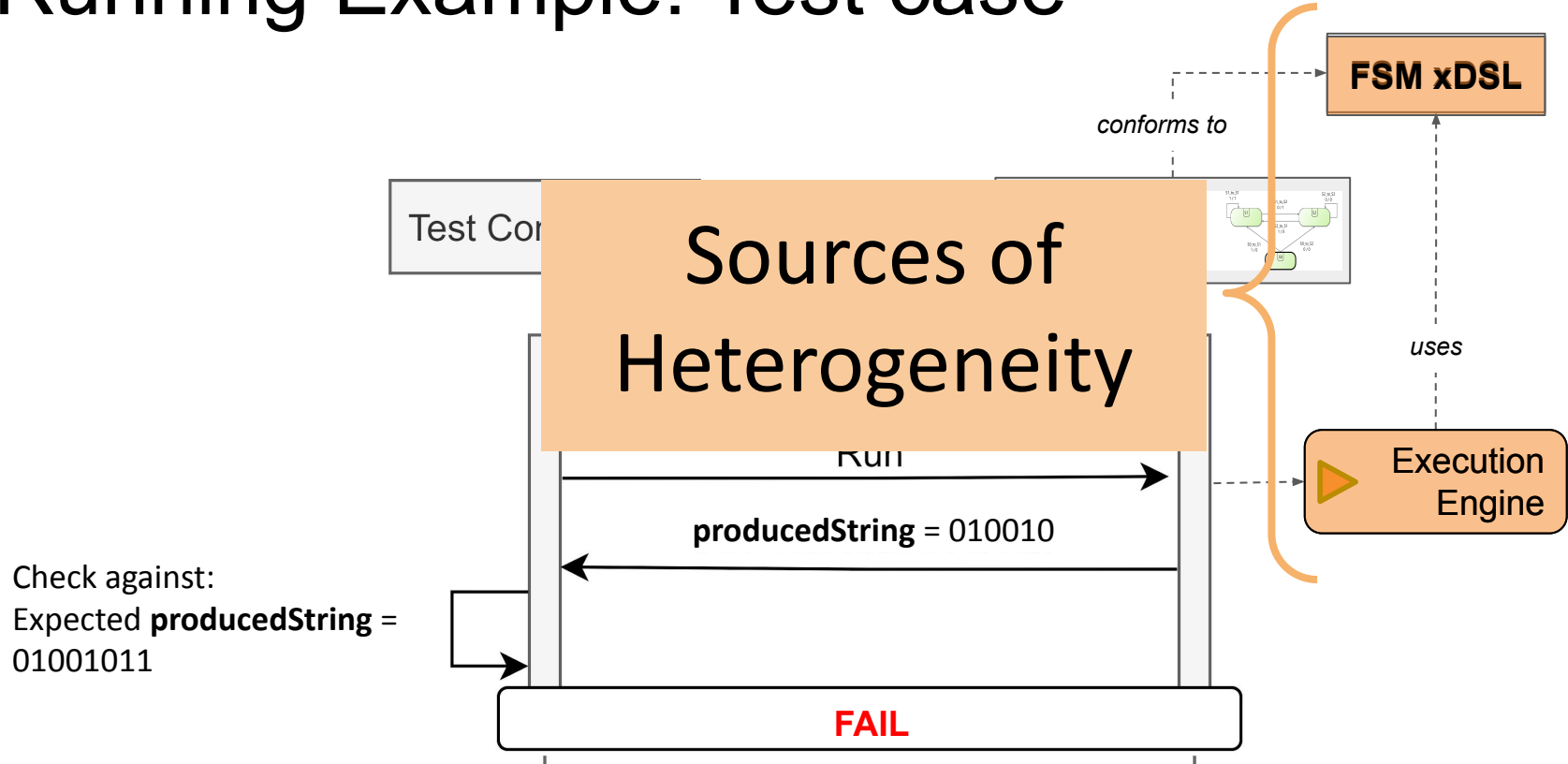


Executable FSM Model: Bit shifting FSM The model under test



Input data: unprocessedString = 10010110
Expected output: producedString = 01001011

Running Example: Test case



Main observation

Lack of a generic testing approach applicable to a wide range of xDSLs

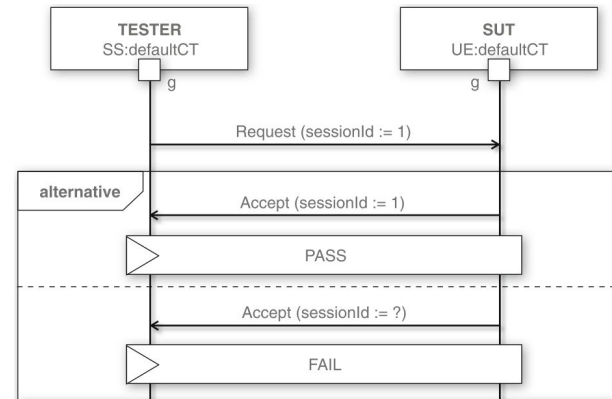
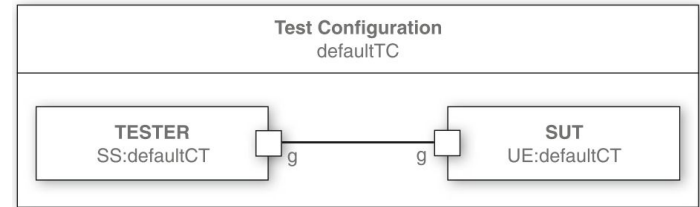
A generic testing approach must provide a *test language*

- Allowing the domain expert to write test cases using *domain* concepts
- Connected to the *semantics* of the xDSL to execute test cases on the models
- Providing facilities to analyze the runtime state of the tested model

Problem Statement: TDL Testing Language

Candidate: Test Description Language (TDL) [1]

- ✓ A standardized language for the specification of test descriptions
- ✓ Not specific to any specific GPL or DSL
- ✓ Designed as a simple language for testers lacking programming knowledge, so a good fit for domain experts

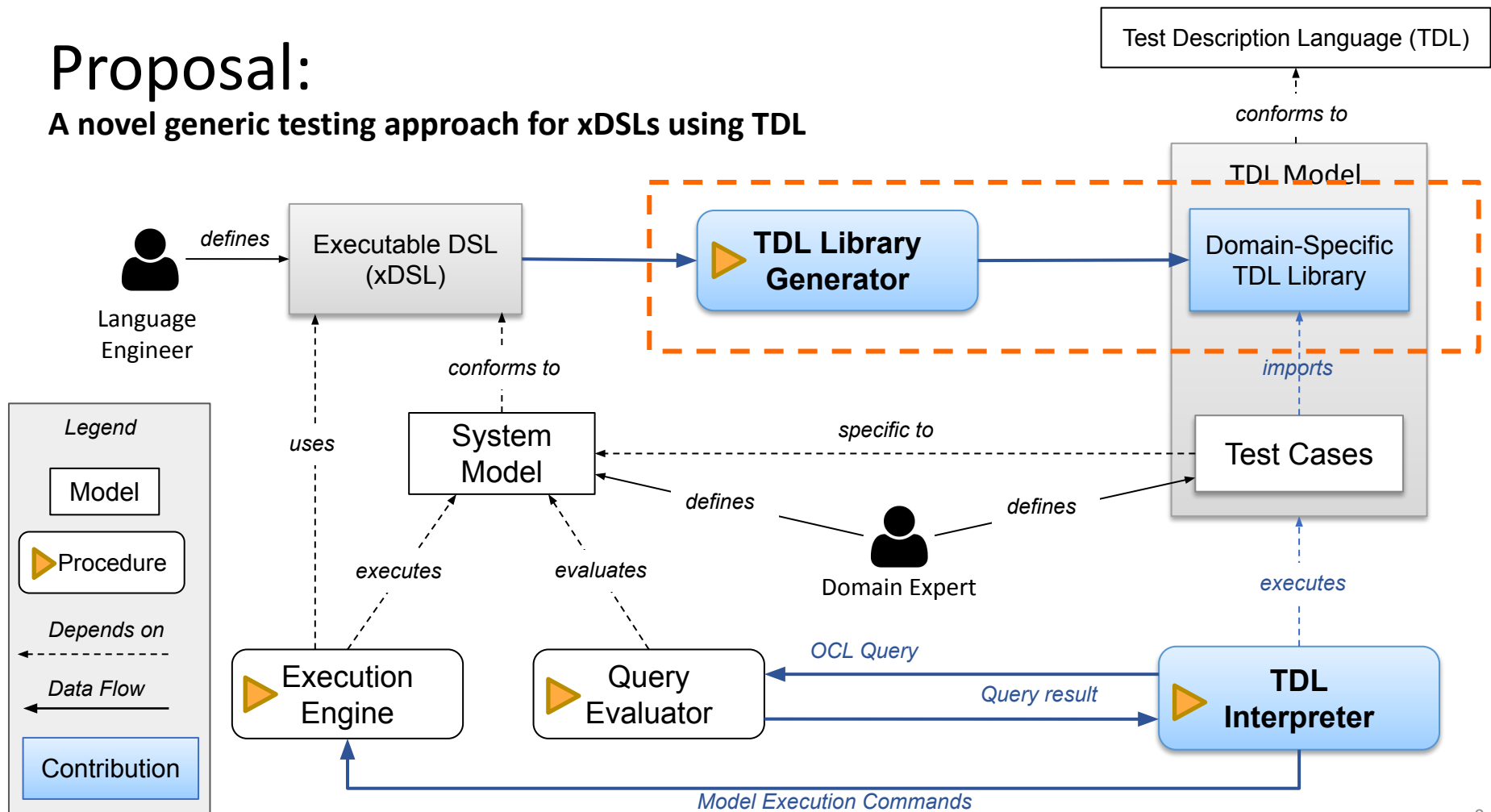


Problem Statement: TDL Limitations

- *Too generic*: the domain expert must first define the required domain-specific concepts, and then write test cases
- No clear way to enable TDL test cases to execute models conforming to an xDSL (i.e., the model under test)
- Relying on a simple representation of the expected behavior of the SUT

Proposal:

A novel generic testing approach for xDSLs using TDL



TDL Test Cases using Model-Execution Commands

Sending test input data: the bit shifting FSM in a runtime state

Request for model execution by sending **'runModel'** command, and get the current state of the model using **'getModelState'**, both provided by *common package*

Oracle with expected output data

```
1 Package bitShiftingFSM_TestSuite {
2   Import all from common;
3   Import all from fsmSpecificTypes;
4   Import all from testConfiguration;
5
6   StateMachine stateMachineNewState(
7     _name = "BitShifting");
8   State S2 (_name = "S2");
9
10  Test Description bitShiftingGenericTest uses
11  configuration fsmConfiguration{
12    tester.genericGate sends
13    stateMachineNewState
14    (unprocessedString = "10010110")
15    to fsm.genericGate;
16    tester.genericGate sends runModel
17    to fsm.genericGate;
18    tester.genericGate sends getModelState
19    to fsm.genericGate;
20    fsm.genericGate sends stateMachineNewState
21    (producedString = "01001011")
22    to tester.genericGate;
23  }
```

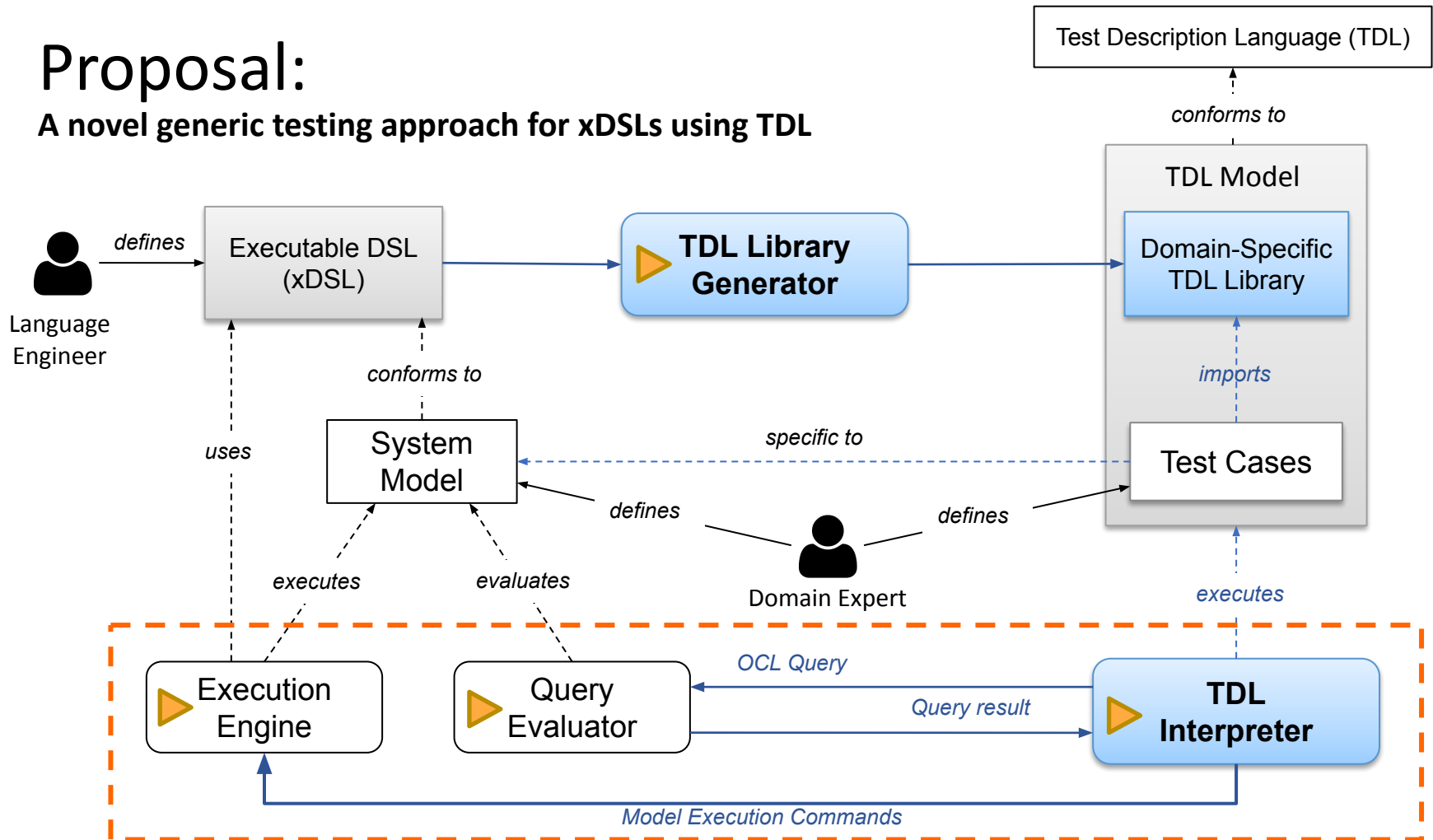
TDL Test Cases using OCL Queries

Using OCL queries to check the value of a specific dynamic feature after executing the model in a specific runtime state

```
22 Test Description bitShiftingOclTest uses
23 configuration fsmConfiguration{
24     tester.genericGate sends
stateMachineNewState
25     (unprocessedString = "000101010")
26     to fsm.genericGate;
27     tester.genericGate sends runModel
28     to fsm.genericGate;
29     tester.oclGate sends oclQuery
30     (query = "self.currentState")
31     to fsm.oclGate;
32     fsm.oclGate sends S2 to tester.oclGate;
33 }
```

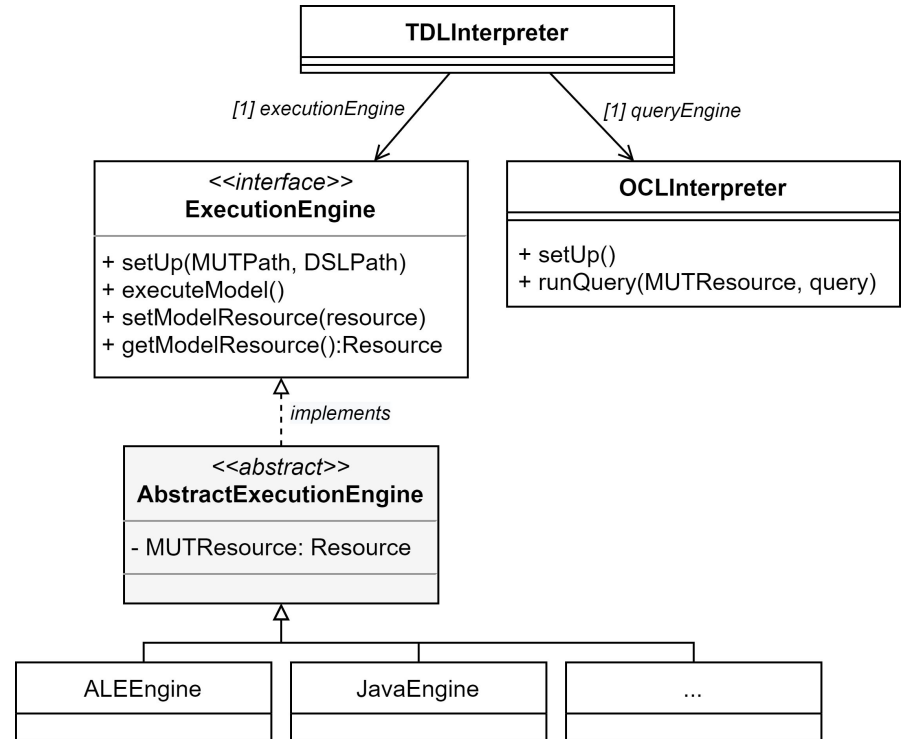
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TDL Interpreter

- Connected to two external components
 - Execution Engine
 - OCL Query Evaluator (Using Eclipse OCL API)
- Definition of an interface to decouple the TDL Interpreter from various execution engines



Tool Support

The screenshot displays the GEMOC Studio interface with several key components:

- Project Explorer (2):** Shows the project structure for `BitShifting_Test`, including `generated` files like `common.tdlan2`, `fsmSpecificTypes.tdlan2`, `testConfiguration.tdlan2`, `representations.aird`, and `testSuite.tdlan2`.
- TDL Test Results (5):** A table showing test outcomes:

Test case	Result	Description
test1	FAIL	
Message#1	PASS	New value is
Message#2	PASS	The model u
Message#3	PASS	The current:
Message#4	FAIL	The expected
test2	PASS	
test3	INCONCLUSIVE	

The expected data is: 01001011, but the current data is: 010010
- Code Editor (3):** Shows the implementation of `test1` in `testSuite.tdlan2`, including state machine creation and test actions like `send`, `oclQuery`, and `sendS2`.
- Sequence Diagram (4):** Illustrates the interaction between `SUT` and `Tester`. Messages include `stateMachineNewState`, `runModel`, `getModelState`, and `stateMachineNewState` with associated data and guard conditions.
- State Machine Diagram (1):** Shows a state machine with states `S0`, `S1`, and `S2`. Transitions are labeled with guard conditions and actions, such as `S1_to_S2 0/1` and `S2_to_S1 1/0`.
- Gemoc Engines Status:** Shows the status of engines like `BitShifting.fsm 1[0]` and `testSuite.tdlan2 1...`.

Evaluation

xDSL	DSL size		Model	TDL Library Size (LoC generated)	Test suite size (LoC)
	Abstract syntax size (n. of EClasses)	Semantics size (LoC)			
xFSM	3	K3: 110 ALE: 90	TrafficLight	111	4
			BitShifting		4
			EdgeDetector		8
			ToLowerCase		5
			ToUpperCase		5
xArduino	59	K3: 667 ALE:421	Servo9g	259	4
			ActiveWaitIR		4
			TurnOnLED		2
			ServoIrButton		4
xBPMN	39	ALE: 318	VerifyUserAccess	202	2
			PromoteEmployee		4

Future Work

- Exploring the approach on more complex xDSLs
 - Testing models with more complex runtime states
- Extending the proposed approach
 - Supporting models conforming to several interconnected xDSLs
 - Providing testing support for Reactive DSLs

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